

**REVISION LEVEL 03**

DATE: 91-07-09

**Reason:** Offset at time base  $\leq 20 \mu\text{s}/\text{div}$ .

ITEM	FROM	TO	
C1222	-	4822 126 10004	120PF, C1222 added, parallel to R1232.

**REVISION LEVEL 04**

DATE: 91-07-12

**Reason:** Other type number for L1202 used, same ordering code.

ITEM	FROM	TO	
L1202	5322 157 63651	5322 157 63651	HF30ACB- 322513

**REVISION LEVEL 05**

DATE: 91-11-04

**Reason:** New printed circuit board version. The board number is located next to display driver D1403.

ITEM	FROM	TO	
BOARD	4022 245 03645	4022 245 03646	

**REVISION LEVEL 06**

DATE: 91-12-16

**Reason 1:** Hang up at loading software version V3.15

ITEM	FROM	TO	
R1233	-	4822 050 11003	10K, added; See fig.10.1a

**Reason 2:** New ordering code for H1202

ITEM	FROM	TO	
H1202	5322 130 62923	4822 130 62297	SFH309F-04

**REVISION LEVEL 07**

DATE: 92-02-17

**Reason:** ANALOG BOARD A2 REVISION LEVEL 17 and higher supplies +5.3V to the DIGITAL BOARD A1. Older ANALOG BOARD A2 versions supply +5V. If the +5.3V is supplied and the modifications described below are not implemented, the display contrast will be bad, and the +12V programming voltage will not be accepted.

You must carry out modifications **REVISION LEVEL 07** and **08** if you combine a DIGITAL BOARD A1 with an ANALOG BOARD A2 provided with a new analog ASIC D2301 (OQ0308N6). See also "Hardware modification overview ANALOG BOARD A2" **REVISION LEVEL 17**.